

## Xilinx Zynq®-7000 All Programmable SoC ZC706 Evaluation Kit

### EVAL-TPG-ZYNQ3



#### Overview

The Zynq®-7000 All Programmable SoC ZC706 Evaluation Kit Optimized for JESD204B provides a data capture platform for many of the Radioverse families of wideband transceiver evaluation boards. A single 12 V power supply provides power for both the Zynq® and the Radioverse evaluation boards. The data capture platform interfaces to the RadioVerse evaluation boards through a variety of interfacing standards including JESD207 and JESD204B. The JESD204B interface on the Zynq® evaluation system supports up to 12.5 Gbps lane rates. A variety of interface options allows the evaluation kit to interface directly to a PC monitor, keyboard, and mouse as well to a PC running the Transceiver Evaluation or Prototyping Software Packages.

#### Features and Benefits

- Upgraded with -3 speed grade XC7Z045 FFG900 supporting up to 12.5 Gbps JESD204B Lane Rates
- 1 GB (PS) DDR3 Component Memory
- 1 GB (PL) SODIM Memory
- GigE Ethernet
- USB Host
- FMC LPC and HPC Expansion Ports
- 12 V Adapter powers Zynq® Evaluation Kit
  - Provides Power for RadioVerse Transceiver Evaluation Boards via FMC Connector

#### Package Contents

- ZC706 evaluation board upgraded with XC7Z045 FFG900-SOC
- Power supply
- For the user guide with AD9371 and AD9375, please refer to the section “AD9371 Demonstration System Overview” of the AD9371 User Guide UG-992 in the [ADI EngineerZone](#).

#### Additional components to be ordered separately:

- Ethernet Cable

#### Development Kit

Richardson RFPD has released a development kit that combines the DE705 with ADI ADRV9375 transceiver evaluation board and ADI EVAL-TPG-ZYNQ3 SoC board. The kit includes the boards, documentation, and most accessories. For more information please see [ADI-DPD-DEVKIT](#).